



CENTRE FOR DEVELOPMENT OF IMAGING TECHNOLOGY

(Under Government of Kerala)
CEP EXAMINATION-OCTOBER 2019
Subject: Computer Architecture
PGDCA I / ADCHNE I

Register No.

Time: 3 Hours

Maximum Marks: 100

I. Fill in the blanks. Answer ALL questions.

(10 × 1 = 10)

1. Most computer operation are executed in the _____ of the processor.
2. DMA stands for _____
3. _____ is the time between the start of a process and its completion.
4. MDR stands for _____
5. _____ is the simplest mode of addressing.
6. SJF stands for _____
7. PROM stands for _____
8. Parallel processing is also called _____
9. POST stands for _____
10. In terms of Registers, IR stands for _____

II. Explain briefly. Answer any TEN questions.

(10 × 4 = 40)

1. Briefly explain about hardware and software.
2. Write a short note on stack and queue.
3. Briefly explain about bus arbitration.
4. Write the importance of cache memory.
5. What do you mean by signed numbers?
6. Write a short not SMPS.
7. What do you mean by Booth's algorithm?
8. Briefly explain about deadlock.
9. Write a short note on disk controller.
10. Briefly explain about vector super computer.
11. Write the concept of registers.
12. Briefly explain about DMA.
13. Write a short note on fault tolerant computer.

III. Answer any FIVE questions. Explain in detail.

(5 x 10 = 50)

1. Explain in detail about memory operations.
2. What do you mean by addressing mode? Explain different types.
3. What do you mean by interrupts? Explain in detail.
4. Explain different mother board components in detail.
5. What are the different types of micro instruction? Explain.
6. Explain different types of auxiliary storage devices with examples.
7. Explain the followings
 - a) Chipset
 - b) BIOS
 - c) Inter process communication
 - d) USB
