



CENTRE FOR DEVELOPMENT OF IMAGING TECHNOLOGY

(Under Government of Kerala)

CEP EXAMINATION-FEBRUARY 2019

Subject: Computer Architecture

PGDCA I / ADCHNE I

Register No.

Time: 3 Hours

Maximum Marks:100

I. Fill in the blanks. Answer ALL questions.

(10 x 1 = 10)

1. _____ languages are low level languages for programming computers.
2. _____ is a portion of code with in larger program.
3. The performance of cache memory is expressed in terms of _____
4. A magnetic tape is made up of _____ material.
5. MDR stands for _____
6. _____ is a set of data processing elements connected in series.
7. BIOS stands for _____
8. _____ is defined as the time between the start of a process and its completion.
9. DMA stands for _____
10. _____ contains the address of an instruction to be fetched.

II. Explain briefly. Answer any TEN questions.

(10 x 4 = 40)

1. Write a short note on registers.
2. Define stack and queue.
3. Write a note on logic gate.
4. What are assembler directives?
5. Write a short note on SMPS.
6. Briefly explain about hard disk.
7. Write the process of fetching word from memory.
8. What do you mean by fault tolerant computer?

9. Write a short note on direct memory access.
10. Distinguish between synchronous bus and asynchronous bus.
11. What do you mean by micro instruction?
12. Write a note on computer chip set.
13. What you meant by deadlock?

III. Answer any FIVE questions. Explain in detail.

(5 x 10 = 50)

1. Explain different types of addressing modes in detail.
2. What do you mean by interrupt? Explain in detail.
3. What are the different ports available in motherboard?
4. Explain in detail about memory organization.
5. Explain the concept of multiprocessors.
6. Explain Booth's multiplication algorithm with flow chart.
7. Briefly explain the following
 - a) Cache memory
 - b) Parallel Port
 - c) USB Port
 - d) NIC card
